



September 27, 2024

Announcement of Technical Presentation at SWTest Asia 2024

[TESEC Corporation](#) and [Tokyo Seimitsu Co., Ltd.](#) will jointly present a technical paper at [SWTest Asia 2024](#), to be held in Fukuoka, Japan, as follows:

- **Event Period:** Thursday, October 24 – Saturday, October 26, 2024
- **Location:** Hilton Fukuoka Sea Hawk
- **Presentation Date:** Friday, October 25, 2024, 14:00–14:30
- **Theme:** “Challenges and Solutions in Wafer Testing from the Perspective of Power Semiconductor Back-End Processes and Modules”
- **Presenter:** Kai Suzuki, TESEC Corporation

* We would like to extend our gratitude to Prof. Iwamuro of Tsukuba University for his invaluable support in writing the technical paper.

[About SWTest Asia 2024]

The Semiconductor Wafer Test Conference ([SWTest](#)) is the only international industry event that focuses on semiconductor wafer and die-level testing, with a history of over 30 years in the United States. Following last year’s event in Taiwan, it will be held in Japan for the first time this year as ‘SWTest Asia 2024.’